

#12

42390.P5346

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Thomas J. Holman

Serial No.: 09/023,170

Filed: February 13, 1998

For: MEMORY SYSTEM INCLUDING A
MEMORY MODULE HAVING A
MEMORY MODULE CONTROLLER
INTERFACING BETWEEN A SYSTEM
MEMORY CONTROLLER AND
MEMORY DEVICES OF THE
MEMORY MODULE (as amended)



Examiner: Verbrugge, K.

Art Unit: 2751

Assistant Commissioner for Patents
Washington, D.C. 20231

RESPONSE

Sir:

In response to the Office Action mailed May 17, 2000, applicant respectfully requests the Examiner to consider the following remarks.

FIRST CLASS CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231

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